

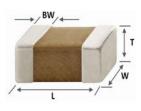


## **Specification of Automotive MLCC** (Reference sheet)

- Supplier : Samsung electro-mechanics
- Product : Multi-layer Ceramic Capacitor
- Samsung P/N : CL21C101JC61PNC
- Description : CAP, 100pF, 100V, ± 5%, C0G, 0805
- AEC-Q200 Qualified

## A. Dimension

Dimension



Size	0805 inch
L	2.00±0.10 mm
W	1.25±0.10 mm
Т	0.60±0.10 mm
BW	0.50 +0.20/-0.30 mm

## B. Samsung Part Number

<u>CL</u> ①	<mark>21</mark> ②	<mark>C</mark> 3	<u>101</u> ④	<mark>ل</mark> 3	<u>C</u> 6	<u>6</u> 7	<u>1</u> ®	<u>P</u> 9	<u>N</u> 10	<mark>C</mark> 10	
Samsung	Multi-la	yer Cer	amic Capa	acitor							

1 Series	Samsung Multi-layer Ceramic Capacitor		
② Size	0805 (inch code)	L: 2.00±0.10 mm	W: 1.25±0.10 mm
③ Dielectric	COG	⑧ Inner electrode	Ni
Capacitance	100 pF	Termination	Cu
5 Capacitance	± 5%	Plating	Sn 100% (Pb Free)
tolerance		9 Product	Automotive
6 Rated Voltage	100 V	Special code	Normal
⑦ Thickness	0.60±0.10 mm	1 Packaging	Cardboard Type, 7" Reel

## C. Reliability Test and Judgement condition

	Performance	Test condition			
High Temperature	Appearance : No abnormal exterior appearance	Unpowered, 1,000hrs @ Max. temperature			
• •	Capacitance Change : Within $\pm 2.5\%$ or $0.25\text{ pF}$	Measurement at 24±2hrs after test conclusion			
Exposure	whichever is larger				
	Q : 1,000 min.				
	IR : More than 10,000 <sup>M</sup> Ω or 500 <sup>M</sup> Ω×μ <sup>F</sup>				
	Whichever is smaller				
Temperature Cycling	Appearance : No abnormal exterior appearance	1,000Cycles			
	Capacitance Change : Within ±2.5% or 0.25pF	Measurement at 24±2hrs after test conclusion			
	whichever is larger				
	Q : 1,000 min.	1 cycle condition : -55+0/-3 $^\circ C(30\pm 3min) \rightarrow$ Room Temp. (1min)			
	IR : More than 10,000 M $_{\Omega}$ or 500 M $_{\Omega} \times \mu F$	$\rightarrow$ 125+3/-0 °C (30±3min) $\rightarrow$ Room Temp. (1min)			
	Whichever is smaller				
Destructive Physical	No Defects or abnormalities	Per EIA 469			
Analysis					
Humidity Bias	Appearance : No abnormal exterior appearance	1,000hrs 85°C/85%RH, Rated Voltage and 1.3~1.5V,			
-	Capacitance Change : Within ±2.5% or 0.25pF	Add 100kohm resistor			
	whichever is larger				
	Q : 200 min.	The charge/discharge current is less than 50mA.			
	IR : More than 500 <sup>MΩ</sup> or 25 <sup>MΩ</sup> ×μ <sup>F</sup>				
	Whichever is smaller				
High Temperature	Appearance : No abnormal exterior appearance	1,000hrs @ 125 ℃, 200% Rated Voltage.			
Operating Life	Capacitance Change : Within $\pm 3\%$ or $0.3pF$	Measurement at 24±2hrs after test conclusion			
	whichever is larger	The charge/discharge current is less than 50mA.			
	Q : 350 min.				
	IR : More than 1.000 M $\Omega$ or 50 M $\Omega$ × $\mu$ F				
	,				
	Whichever is smaller				

	Performance	Test condition				
External Visual	No abnormal exterior appearance	Microscope ('10)				
Physical Dimensions	Within the specified dimensions	Using The calipers				
Mechanical Shock	Appearance : No abnormal exterior appearance	Three shocks in each direction should be applied along				
	Capacitance Change : Within ±2.5% or 0.25pF	3 mutually perpendicular axes of the test specimen (18 shocks)				
	whichever is larger	Peak value Duration Wave Velocity				
		1,500G 0.5ms Half sine 4.7m/sec				
	Q, IR : Initial spec.					
Vibration	Appearance : No abnormal exterior appearance	5g's for 20min., 12cycles each of 3 orientations,				
	Capacitance Change : Within ±2.5% or 0.25pF	Use 8"×5" PCB 0.031" Thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted				
	whichever is larger					
		within 2" from any secure point. Test from $10~2,000$ Hz.				
	Q, IR : Initial spec.					
Resistance to	Appearance : No abnormal exterior appearance	preheating : 150°C for 60~120 sec.				
Solder Heat	Capacitance Change : Within ±2.5% or 0.25pF	Solder pot : 260±5℃, 10±1sec.				
	whichever is larger					
	Q, IR : Initial spec.					
ESD	Appearance : No abnormal exterior appearance	AEC-Q200-002 or ISO/DIS10605				
	Capacitance Change : Within ±2.5% or 0.25pF					
	whichever is larger					
<b>0         </b>	Q, IR : Initial spec. 95% of the terminations is to be soldered	a) Preheat at 155 °C for 4 hours, Immerse in solder for 5s at 245±5 °C				
Solderability	evenly and continuously	b) Steam aging for 8 hours, Immerse in solder for 5s at $245\pm5^{\circ}$				
		c) Steam aging for 8 hours, Immerse in solder for 120s at 24015 °C				
		solder : a solution ethanol and rosin				
Electrical	Capacitance : Within specified tolerance	The Capacitance / D.F. should be measured at 25 $^\circ\!\!\!\mathrm{C}$ ,				
Characterization	Q : 1,000 min.	1 <sup>Mlz</sup> ± 10%, 0.5~5 Vrms				
	IR(25℃): More than 100,000 <sup>M</sup> or 1,000 <sup>M</sup> ×µF	I.R. should be measured with a DC voltage not exceeding				
	Whichever is smaller	Rated Voltage @25℃, @125℃ for 60~120 sec.				
	IR(125℃): More than 10,000 <sup>M</sup> or 100 <sup>M</sup> ×µ <sup></sup>					
	Whichever is smaller					
	Dielectric Strength	Dielectric Strength : 200% of the rated voltage for 1~5 seconds				
Board Flex	Appearance : No abnormal exterior appearance	Bending to the limit, 3 mm for 60 seconds				
	Capacitance Change : Within ±5% or 0.5pF					
	whichever is larger					
Terminal	Appearance : No abnormal exterior appearance	18 N, for 60 sec.				
Strength(SMD)	Capacitance Change : Within ±2.5% or 0.25pF					
	whichever is larger					
Beam Load	Destruction value should be exceed 20 N	Beam speed : 0.5±0.05 mm/sec				
Temperature	C0G					
Characteristics	From -55 $^\circ C$ to 125 $^\circ C$ , Capacitance change should	be within 0±30ppm/°C				

D. Recommended Soldering method :

Reflow ( Reflow Peak Temperature : 260 +0/-5  $^\circ C$  , 30sec. ), Meet IPC/JEDEC J-STD-020 D Standard

A Product specifications included in the specifications are effective as of March 1, 2013.

Please be advised that they are standard product specifications for reference only.

We may change, modify or discontinue the product specifications without notice at any time.

So, you need to approve the product specifications before placing an order.

Should you have any question regarding the product specifications, please contact our sales personnel or application engineers.