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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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## RENESAS

# MOS FIELD EFFECT TRANSISTOR NP88N04NUG

ORDERING INFORMATION PART NUMBER

NP88N04NUG

### SWITCHING N-CHANNEL POWER MOSFET

#### DESCRIPTION

The NP88N04NUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

#### FEATURES

- Channel temperature 175 degree rating
- Super low on-state resistance  $R_{DS(on)} = 3.4 \ m\Omega \ MAX. \ (V_{GS} = 10 \ V, \ I_D = 44 \ A)$
- Low Ciss: Ciss = 9510 pF TYP. (VDs = 25 V)

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGS = 0 V)	VDSS	40	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	D(DC)	±88	А
Drain Current (pulse) <sup>Note1</sup>	D(pulse)	±352	А
Total Power Dissipation	PT1	1.8	W
Total Power Dissipation (Tc = $25^{\circ}$ C)	Pt2	200	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Repetitive Avalanche Current Note2	IAR	56	А
Repetitive Avalanche Energy Note2	Ear	314	mJ
<b>Notes 1.</b> PW $\leq$ 10 $\mu$ s, Duty Cycle $\leq$ 1%			
<b>2.</b> $T_{ch} \le 150^{\circ}C$ , $V_{DD} = 20 V$ , $R_G = 25$	5 Ω, Vgs = 2	$20 \rightarrow 0 \text{ V}$	

#### THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	0.75	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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(TO-262)

PACKAGE

TO-262



Document No. D17400EJ1V0DS00 (1st edition) Date Published November 2005 NS CP(K) Printed in Japan

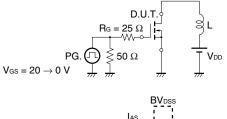
ELECTRICAL CHARACTERISTICS (TA = 25°C)

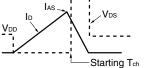
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1	μA
Gate Leakage Current	lgss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±100	nA
Gate to Source Threshold Voltage	VGS(th)	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 44 A	27	51		S
Drain to Source On-state Resistance Note	RDS(on)	Vgs = 10 V, Id = 44 A		2.6	3.4	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 25 V		9510	15000	pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		880	1370	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		570	990	pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 44 A		43	100	ns
Rise Time	tr	V <sub>GS</sub> = 10 V		104	260	ns
Turn-off Delay Time	td(off)	Rg = 0 Ω		107	220	ns
Fall Time	tr			22	60	ns
Total Gate Charge	QG	V <sub>DD</sub> = 32 V		171	250	nC
Gate to Source Charge	Q <sub>GS</sub>	V <sub>GS</sub> = 10 V		38		nC
Gate to Drain Charge	Qgd	I <sub>D</sub> = 88 A		58		nC
Body Diode Forward Voltage Note	VF(S-D)	IF = 88 A, VGS = 0 V		0.94	1.5	V
Reverse Recovery Time	trr	IF = 88 A, VGS = 0 V		51		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		67		nC

Note Pulsed

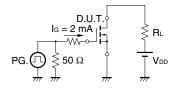
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

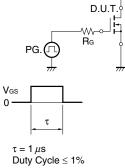
#### **TEST CIRCUIT 2 SWITCHING TIME**

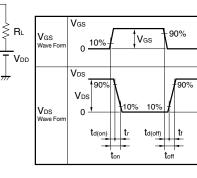




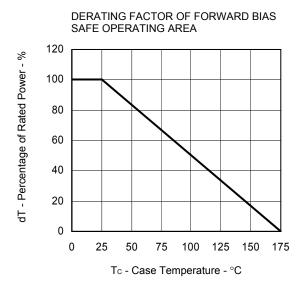
#### TEST CIRCUIT 3 GATE CHARGE

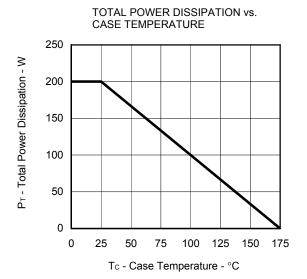




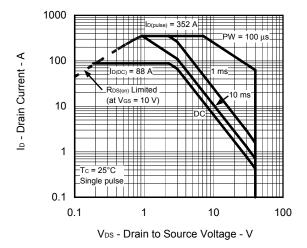


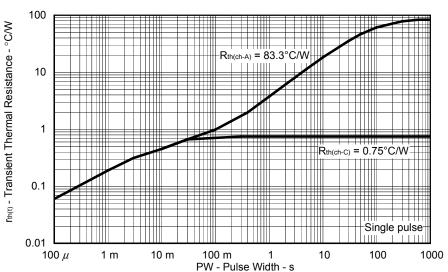
#### TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)



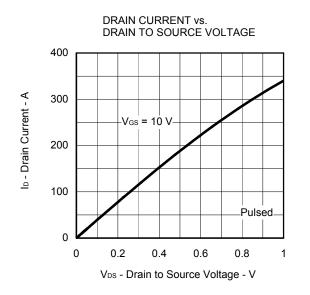


FORWARD BIAS SAFE OPERATING AREA

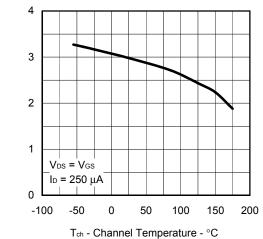


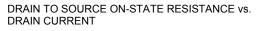


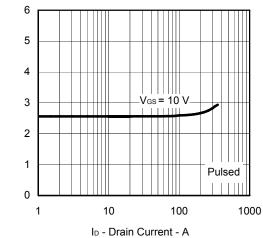
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



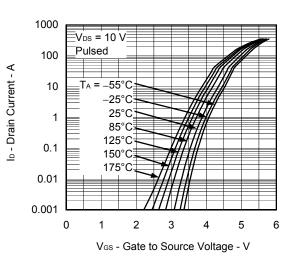




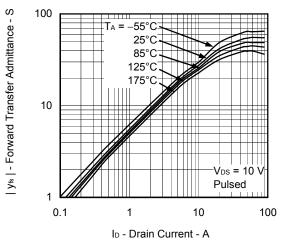




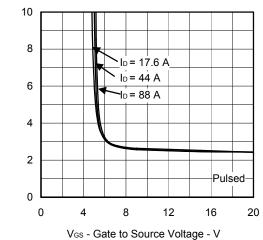
FORWARD TRANSFER CHARACTERISTICS

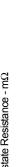


FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



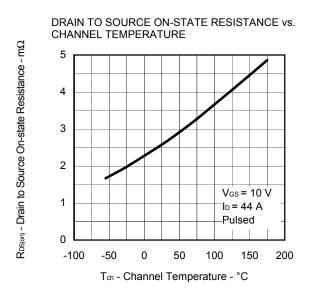
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



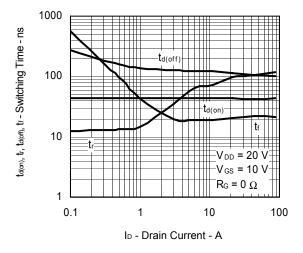


V<sub>GS(th)</sub> - Gate to Source Threshold Voltage - V

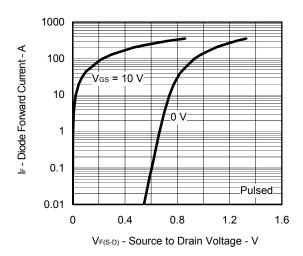
 $R_{DS(m)}$  - Drain to Source On-state Resistance - m $\Omega$ 



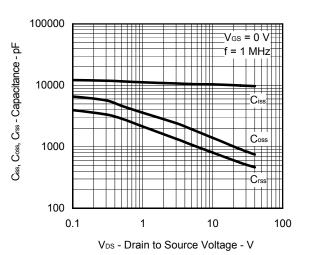
SWITCHING CHARACTERISTICS

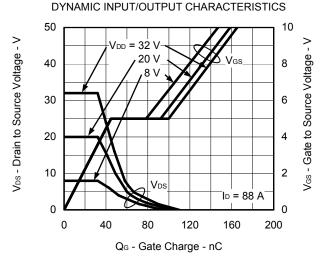


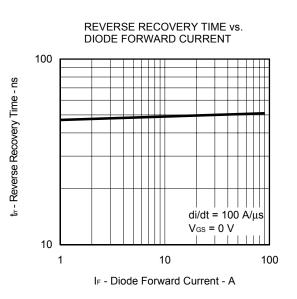
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

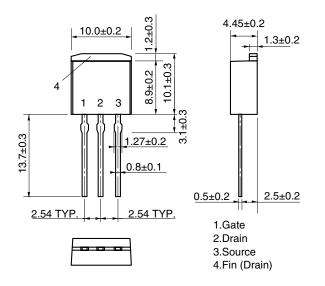




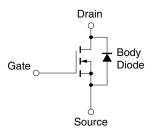


#### PACKAGE DRAWING (Unit: mm)

#### TO-262 (Revised)



EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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