



Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C
-30V	5Ω @ V _{GS} = -4.5V	
	6Ω @ V _{GS} = -2.5V	-0.2A
	7Ω @ V _{GS} = -1.8V	-0.2A
	10Ω @ V _{GS} = -1.5V	

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low Package Profile, 0.42mm Maximum Package height
- 0.62mm x 0.62mm Package Footprint
- Low On-Resistance
- Very low Gate Threshold Voltage, 1.0V max
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

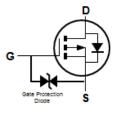
Mechanical Data

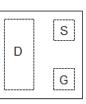
- Case: X2-DFN0606-3
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper leadframe Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.001 grams (approximate)





Bottom View





Equivalent Circuit

Top View Package Pin Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP32D9UFZ-7B	X2-DFN0606-3	10K/Tape & Reel

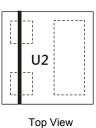
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Bar Denotes Gate and Source Side U2 = Product Type Marking Code

JEW PRODUCT



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±10	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	-200 -100	mA
Pulsed Drain Current (Note 6)	•		I _{DM}	-500	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	PD	390	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ ext{ heta}JA}$	322	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	- ,		- 71-				
Drain-Source Breakdown Voltage	BV _{DSS}	-30		_	V	V _{GS} = 0V, I _D = -250µA	
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	I _{DSS}	_	—	100	nA	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	—	±10	μA	V_{GS} = ±10V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 7)					-		
Gate Threshold Voltage	V _{GS(th)}	-0.4		-1.0	V	V_{DS} = V_{GS} , I_D = -250 μ A	
				5		V_{GS} = -4.5V, I_{D} = -100mA	
		_	_	6		V _{GS} = -2.5V, I _D = -50mA	
Static Drain-Source On-Resistance	R _{DS (ON)}	—	_	7	Ω	V _{GS} = -1.8V, I _D = -20mA	
		_	—	10		V _{GS} = -1.5V, I _D = -10mA	
	-	_	6	—		V _{GS} = -1.2V, I _D = -1mA	
Diode Forward Voltage	V _{SD}	_	-0.75	-1.0	V	V _{GS} = 0V, I _S = -10mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		22.5	—	pF		
Output Capacitance	Coss		2.9	—	pF	[−] V _{DS} = -15V, V _{GS} = 0V, −f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	2.1	—	pF		
Total Gate Charge	Qg	_	0.35	—	nC		
Gate-Source Charge	Q _{gs}	_	0.06	_	nC	−V _{GS} = -4.5V, V _{DS} =- 15V, −I _D = -200mA	
Gate-Drain Charge	Q _{gd}	_	0.09	_	nC	1D20011A	
Turn-On Delay Time	t _{D(on)}		3.1	—	ns		
Turn-On Rise Time	tr		2.3	—	ns	V _{DD} = -10V, V _{GS} = -4.5V,	
Turn-Off Delay Time	t _{D(off)}		19.9	—	ns	$R_{G} = 6\Omega, I_{D} = -200 \text{mA}$	
Turn-Off Fall Time	t _f		10.5	_	ns	1	

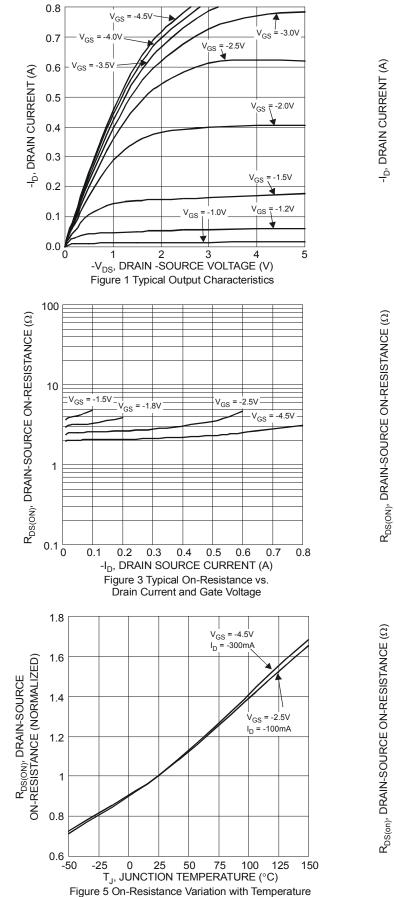
Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

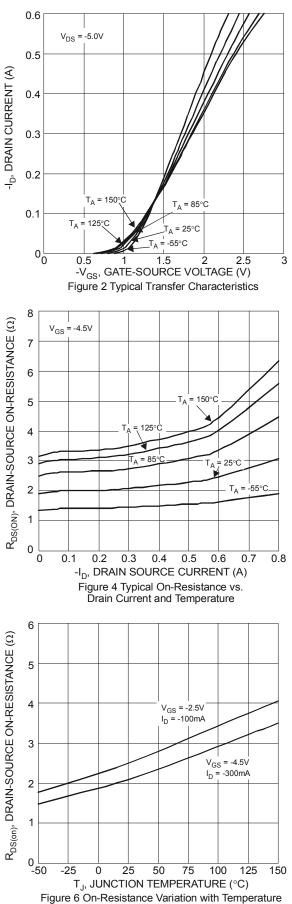
Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



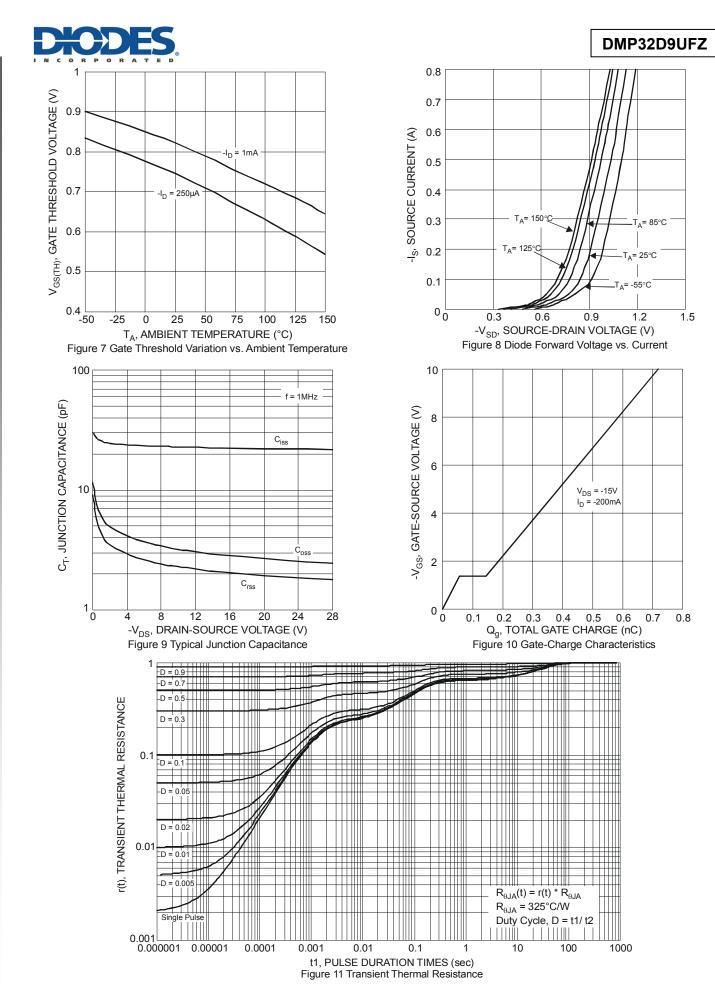
DMP32D9UFZ





NEW PRODUCT

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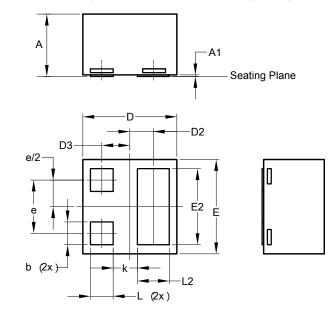


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Package Outline Dimensions

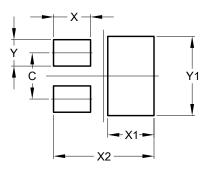
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN0606-3					
Dim	Min	Max	Тур		
Α	0.36	0.42	0.39		
A1	0	0.05	0.02		
b	0.10	0.20	0.15		
D	0.57	0.67	0.62		
D2	0.155 BSC				
D3	0	0.185 BSC			
E	0.57	0.67	0.62		
E2	0.40	0.60	0.50		
е	0.35 BSC				
k	0.16 REF				
L	0.09	0.21	0.15		
L2	0.11	0.31	0.21		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	0.350			
Х	0.280			
X1	0.350			
X2	0.760			
Y	0.200			
Y1	0.600			



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